

**IN THE SPECIFICATION (INCLUDING THE ABSTRACT):**

Please replace section **BRIEF DESCRIPTION OF THE DRAWINGS** with the following rewritten section:

**BRIEF DESCRIPTION OF THE DRAWINGS**

FIG. 1 is a circuit diagram of a conventional DAC driving structure.

FIG. 2 is a waveform modulated by a conventional PWM driving structure.

FIG. 3 is a circuit diagram of a conventional PWM driving structure.

FIG. 4 is a driving waveform modulated by the driving method of speaker of the present invention.

FIG. 5 is another driving waveform modulated by the driving method of speaker of the present invention.

FIG. 6 is a circuit diagram ~~[[of]]~~according to the driving method of speaker of the present invention.

FIG. 7 is another circuit diagram ~~[[of]]~~according to the driving method of speaker of the present invention.

FIG. 8 ~~is~~shows the waveforms of driving signals obtained by driving a sound signal the DAC, PWM, and in accordance with the driving method of the present invention, respectively.

Please replace paragraph beginning at page 6, line 24 with the following rewritten paragraph:

As shown in FIG. 6, the circuit structure employs five higher bits data as PWM control, thus, the waveform of the driving signals has a waveform ~~alike~~similar to the conventional PWM waveform. However, the ~~different~~difference is that three lower bits data are converted into driving signals by DAC with one reference clock width, such that the pulse level of PWM is lowered and the reference clock frequency is also lowered at the same time.

Please replace paragraph beginning at page 7, line 3 with the following rewritten paragraph:

Next, referring to FIG. 7, ~~there is shown the other~~another embodiment of circuit structure ~~[[of]]~~ according to the driving method of the present invention is illustrated. The circuit structure employs three higher bits data (B7-B5) as the D bits data to proceed with pulse height conversion, and employs other five lower bits data (B4-B0) as P bits data to proceed with pulse width modulation. As shown in this drawing, the circuit structure comprises PWM modulation circuit 10', DAC convert circuit 20', and speaker 30.

Please replace paragraph beginning at page 8, line 12 with the following rewritten paragraph:

FIG. 8 ~~are shows~~ the waveform-waveforms of driving ~~signal-signals~~ obtained by the DAC, PWM and present driving method, respectively. FIG. 8(a) is the waveform produced by DAC. FIG. 8 (b) is the waveform produced by PWM. FIG. 8(c) is the waveform obtained by the driving method of the present invention, modulated based on the circuit of FIG. 7, using D bit as higher bit. As shown in the drawings, each pulse

width of the speaker driving waveform, produced by DAC, ~~are~~is of the same; each pulse height of the speaker driving waveform, produced by PWM, ~~are~~is of the same; but in accordance with the present invention, the waveform obtained has different ~~height~~heights and ~~width~~widths.